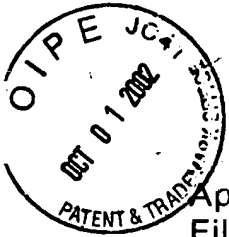


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/388,857
Filing Date September 1, 1999
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L. Schillinger
Attorney's Docket No. MI22-878
Title: Semiconductor Processing Methods Of Forming Transistors,
Semiconductor Processing Methods Of Forming Dynamic Random
Access Memory Circuitry, And Related Integrated Circuitry

RESPONSE TO JULY 1, 2002 FINAL OFFICE ACTION
RESPONSE TO ACCOMPANY CPA FILING

To: Box Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

From: Frederick M. Fliegel, Ph.D.
(Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

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Responsive to the Office Action dated July 1, 2002, Applicant amends
and remarks as follows:

AMENDMENTS